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**PATENT APPLICATION  
TRANSMITTAL LETTER**

ATTORNEY DOCKET NO.:  
**2207/4641**

Assistant Commissioner  
for Patents  
Washington D.C. 20231

**BOX PATENT APPLICATION**

Transmitted herewith for filing is the patent application of

Inventors : **Christopher B. Farmer**

For : **A HORIZONTAL MOUNT FOR A PROCESSOR**

Enclosed are:

1. 12 sheets of specification, 4 sheets of claims and 1 sheet of abstract, and 8 sheets of drawings.
2. Declaration and Power of Attorney.
3. Assignment
4. The filing fee has been calculated as shown below:

	NUMBER FILED	NUMBER EXTRA*	RATE (\$)	FEE (\$)
BASIC FEE				790.00
TOTAL CLAIMS	18 - 20 =	0	22.00	0.00
INDEPENDENT CLAIMS	5 - 3 =	2	82.00	164.00
MULTIPLE DEPENDENT CLAIM PRESENT				270.00
*Number extra must be zero or larger			TOTAL	954.00
If applicant is a small entity under 37 C.F.R. §§ 1.9 and 1.27, then divide total fee by 2, and enter amount here.			SMALL ENTITY TOTAL	.00

**Express Mail No. EI502984853US**

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3. Please charge the required application filing fee of **\$954.00** to the deposit account of **Kenyon & Kenyon**, deposit account number **11-0600**.
5. The Commissioner is hereby authorized to charge any shortfalls in the filing fee and payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 11-0600.  
Any additional filing fees required under 37 C.F.R. 1.16.  
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Any filing fees for the presentation of extra claims under 37 C.F.R. 1.16.  
Any patent application processing fees under 37 C.F.R. 1.17.

Respectfully submitted,

Dated: November 19, 1997

  
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A HORIZONTAL MOUNT FOR A PROCESSOR

FIELD OF THE INVENTION

5

The invention relates to processors in general. More particularly, the invention relates to a method and apparatus for horizontally connecting a microprocessor to a motherboard of a computer.

10

BACKGROUND OF THE INVENTION

Designs in microprocessor architecture are accelerating at an extraordinary pace. It is conventional wisdom that computer technology becomes outdated every 12 months. This is especially true in the area of microprocessor design where users desire more powerful software applications, thus requiring more powerful microprocessors.

In an attempt to solve the problems associated with placing a microprocessor horizontally into a motherboard (e.g, the large amount of space needed on the motherboard to hold this component), a microprocessor referred to as a  
5 single edge connector cartridge (SECC) microprocessor was developed. An example of a SECC microprocessor is illustrated in FIG. 1. As shown in FIG. 1, SECC microprocessor 10 includes a SECC cover 12, a connector 14, and a fan heat sink 16. Connector 14 in this example is a  
10 slot 1 connector. Also shown in FIG. 1 is motherboard 20, on which is mounted a receiving slot 18.

As illustrated in FIG. 1, processor 10 has connector 14 on one side of cartridge 12 which is actually housing the microprocessor. Processor 10 can thus be mounted with  
15 the larger area of cartridge 12 perpendicular or vertical to motherboard 20. By mounting processor 10 vertically to motherboard 20, processor 10 has a smaller footprint and makes room for additional components on motherboard 20. The vertical placement also allows the addition of heat  
20 sink 16 to processor 10.

Although processor 10 solved one set of problems, a new set of problems developed. For example, processor 10 had a smaller footprint which meant precautions were necessary to prevent processor 10 from becoming  
25 disconnected from motherboard 20. Disconnection could occur for any number of reasons, such as movement or vibration during transport or shock if someone accidentally

bumped the housing for motherboard 20. Moreover, the disconnect problem was augmented for processor 10 because of heat sink 16. The overall weight of processor 10 amounts to approximately one pound with the addition of  
5 heat sink 16. The weight of processor 10 means that movement of processor 10 could easily disconnect processor 10 from motherboard 20, and in extreme situations, shatter the relatively fragile structure of motherboard 20.

The problems with processor 10 led to the development  
10 of a structure to hold processor 10 in place to ensure it remained stable. This structure, referred to as a "retention module," however, requires a large footprint, thereby significantly removing the footprint advantage gained using an SECC microprocessor. Furthermore, it adds  
15 yet another structure required in the assembly of a computer, thus increasing the cost of a computer to the consumer. In fact, many conventional retention modules require two separate structures, that is, one to hold processor 10 and a second to support heat sink 16.  
20 Finally, processor 10 requires additional items such as captive fasteners and threaded bridge studs. Consequently, the additional components required by processor 10 add to the overall cost of the computer, as well as increasing the difficulty in assembling the computer during the  
25 manufacturing process.

Yet another problem is associated with processor 10. Motherboard 20 and processor 10 are parts of a computer.

Thus, motherboard 20 and processor 10, as well as other computer components such as a hard drive, floppy disk drive, power supply, and so forth, are surrounded by a casing. This casing is typically made of hard plastic or metal. This means that processor 10 can not be accessed unless the casing is removed. This requirement has several limitations associated with it. For example, there is no assurance by the eventual purchaser of a computer that the processor is the same processor advertised for the computer. Another example is that repairs or upgrades were difficult to accomplish.

In view of the foregoing, it can be appreciated that a substantial need exists for a new processor mount that addresses the aforementioned problems.

#### SUMMARY OF THE INVENTION

One embodiment of the present invention comprises a mount to receive a cartridge processor having an edge connector. The mount includes a motherboard connector mounted on a motherboard oriented to receive the edge connector with the processor in an orientation parallel to the board.

#### BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 illustrates a conventional SECC microprocessor.

FIG. 2 is an outline drawing in front elevation view of a computer suitable for practicing one embodiment of the present invention.

FIG. 3 is a plan view, in outline of the computer of  
5 FIG. 2, showing a horizontal mount in accordance with one embodiment of the invention.

FIG. 4 is a side elevation view of a connector coupled to a motherboard in accordance with one embodiment of the invention.

10 FIG. 5 is a side elevation sectional view of a horizontal mount in accordance with another embodiment of the invention.

FIG. 6 is a partial side elevation view of a computer showing a heat sink for use in accordance with one  
15 embodiment of the invention.

FIG. 7 is a partial side elevation view of a computer having a locking mechanism suitable for use with one embodiment of the invention.

FIG. 8 is a front elevation view of a computer having  
20 another locking mechanism suitable for use with one embodiment of the invention.

#### DETAILED DESCRIPTION

25 The present invention includes a method and apparatus that improves upon conventional methods for connecting a microprocessor to a motherboard. The embodiments of the

present invention reduce the number of components required to form the connection, thereby reducing the overall cost of manufacturing a computer, and simplifying the assembly of the computer. Furthermore, the embodiments of the present invention permit a user to repair and upgrade a microprocessor without having to physically open the casing which houses the microprocessor. In addition, the present invention permits a user to readily ascertain the type of microprocessor used by the computer. This occurs without consuming any more space on a motherboard than required by an SECC microprocessor and retention module as used in the prior art.

Referring now in detail to the drawings wherein like parts are designated by like reference numerals throughout, there is illustrated in FIG. 2 the outline front elevation view of a computer suitable for practicing one embodiment of the present invention. As shown in FIG. 2, the computer has a computer casing 30, with drive bays 32 and 34 to house, for example, a 1.44 megabyte (mb) floppy drive and a hard drive, respectively. Computer casing 30 has a slot 36, which forms an access point for the components located within casing 30. Inserted into slot 36 is a processor 40 (see Fig. 3). Once inserted, a portion of processor 40 is visible to a user from outside casing 30. Processor 40 is similar to processor 10 as shown in FIG. 1. Processor 40 is capable of being viewed from outside casing 30, due to a horizontal mount connector described in more detail in FIG.



3.

FIG. 3 is a plan view, in outline of the computer of FIG. 2, showing a horizontal mount in accordance with one embodiment of the invention. As shown in FIG. 3, a  
5 motherboard 20 is within casing 30. In conventional computers, motherboard 20 is substantially square with a vertical connector as shown in FIG. 1. In this embodiment of the invention, however, a portion of motherboard 20 is removed to accommodate processor 40. In this embodiment of  
10 the invention, motherboard 20 is equipped with guide slots 41 and 42, which correspond to guide rails 43 and 45 of processor 40, respectively, such that processor 40 can be mounted parallel or horizontal to motherboard 20. As used herein, the term "horizontal mount" refers to a mount that  
15 permits a processor to remain substantially parallel to motherboard 20, as opposed to a conventional "vertical mount" that permits a processor to remain substantially perpendicular to motherboard 20. Also, as used herein, the term "parallel" refers to devices that are in the same  
20 plane or in different planes.

As stated before, processor 40 is similar to processor 10. Processor 40 has an SECC casing 48, a connector 46, and guide rails 43 and 45. Processor can also include a heat sink (not shown), which will be discussed in  
25 connection with a different embodiment of this invention.

A microprocessor connector 44 is mounted on motherboard 20. Microprocessor connector 44 is the

interface that carries electrical signals between processor 40 and motherboard 20, and forms a receiving slot or "female" connector that is configured to receive the processor leads or "male" connector (i.e., connector 46) of processor 40. Connector 44 is similar to connector 18 shown in FIG. 1, except that connector 44 is horizontal or parallel to motherboard 20, rather than vertical or perpendicular to motherboard 20 as in conventional microprocessor connectors.

In operation, a user can connect processor 40 into motherboard 20 without removing casing 30. The user inserts processor 40 through slot 36. Guide rails 43 and 45 are designed to match guide slots 41 and 42. When the user begins the insertion process the configuration of the guide rails and slot, as well as slot 36, help align processor 40 for smooth connection of connector 46 into connector 44. Once guide rails 43 and 45 are seated in guide slots 41 and 42, respectively, the user can slide processor 40 into slot 36 until connector 46 seats properly with connector 44. A more detailed description of connector 44 is shown in FIG. 4.

It is worthy to note that in this embodiment of the invention guide slots 41 and 42 are designed to receive guide rails 43 and 45 in accordance with principles well known in the art. Guide slots 41 and 42 and guide rails 43 and 45 help guide connector 46 into connector 44 to ensure the proper physical and electrical connection between

motherboard 20 and processor 40. It can be appreciated, however, that any means for guiding connector 46 into connector 44 falls within the scope of the invention.

FIG. 4 is a side elevation view of a connector coupled  
5 to a motherboard in accordance with one embodiment of the invention. As shown in FIG. 4, connector 44 includes receiving prongs 50 and 52. Connector 44 is mounted on motherboard 20 such that receiving prongs 50 and 52 are substantially parallel to motherboard 20. In this manner,  
10 connector 44 is capable of receiving processor 40 such that processor 40 is parallel to motherboard 20.

FIG. 5 is a side elevation sectional view of a horizontal mount in accordance with another embodiment of the invention. The horizontal mount described with  
15 reference to FIG. 5 is similar to that shown in FIG. 3. In FIG. 5, however, guide rails and guide slots are not mounted on processor 40 and motherboard 20, respectively. Rather, a first guide block 60 and a second guide block 62 are used to align processor 40 as it is being inserted into  
20 slot 36 so that connector 46 seats properly with connector 44. Guide block 60 is positioned to prevent processor 40 from moving up towards the top of casing 30. Guide block 62 is used to keep processor 40 on track from left to right, and preventing processor 40 from moving down towards  
25 the bottom of casing 30.

It is worthy to note that although the various embodiments of the invention described above use specific

means for guiding connector 46 into connector 44, it can be appreciated that any number of guiding means could be used that accomplish this function, and still fall within the scope of the invention.

5 As mentioned previously, conventional processors mount a heat sink on one side of the actual SECC case. In such a case, slot 36 would necessarily be wide enough to accommodate the size of the SECC processor and the heat sink. Moreover, the guiding mechanism would be made of  
10 sufficient strength to allow for the additional weight of the heat sink, as well as constructed to permit connector 46 to seat properly with connector 44 given the additional bulk of the heat sink. Alternatively, the heat sink could be moved from the processor to the internal structure of  
15 the computer within casing 30. An example of this latter type of configuration is shown in FIG. 6.

FIG. 6 is a partial side elevation view of a computer showing a heat sink for use in accordance with one embodiment of the invention. As shown in FIG. 6, a heat  
20 sink 70 is positioned above slot 36 and between connector 44 and the front of case 30. Heat sink 70 is positioned above slot 36 because if heat sink 70 is mounted beneath processor 40, a problem with heat dissipation arises because heat naturally rises. Also located on the front of  
25 case 30 is a fan 72.

Processor 40 is inserted into slot 36, and has a heat sink connector (not shown). Heat sink connector is used to

channel heat from processor 40 to heat sink 70. When connector 46 is seated properly with connector 44, heat sink 70 is positioned so that the heat sink connector makes contact with heat sink 70. In this manner, heat from processor 40 is removed by heat sink 70. Heat dissipation can also be augmented using fan 72.

Security is an important consideration because the size of processor 40 is relatively small, and yet the value of processor 40 is large. This creates motivation for a criminal to simply remove processor 40 without authorization. To avoid this situation, one embodiment of the present invention includes a locking mechanism to prevent unauthorized removal once processor 40 has been locked into place.

FIG. 7 is a partial side elevation view of a computer having a locking mechanism suitable for use with one embodiment of the invention. As shown in FIG. 7, a lock assembly 80 is mounted on the inside of casing 30. Lock assembly 80 includes a key hole 82 for receiving a key (not shown), and a spring 84. Spring 84 can provide, for example, five pounds of force. Thus, once processor 40 is inserted into slot 36, and guided into place so that connector 46 seats properly within connector 44, a user can lock processor 40 into place using lock assembly 80.

FIG. 8 is a front elevation view of a computer having another locking mechanism suitable for use with one embodiment of the invention. As shown in FIG. 8, teeth

locks 90 are mounted on the SEC case for processor 40. In addition, a guiding arrangement similar to that described with reference to FIG. 5 is used, except that guide block 62 is adapted to push processor 40 upwards so that teeth 5 locks 90 prevent processor 40 from being removed. For example, a lock operated spring, or some other mechanism can be used for moving processor 40 upwards to lock it in place, or downwards to unlock it.

Although various embodiments are specifically  
10 illustrated and described herein, it will be appreciated that modifications and variations of the present invention are covered by the above teachings and within the purview of the appended claims without departing from the spirit and intended scope of the invention. For example, although  
15 various examples of guide apparatus are disclosed herein, it can be appreciated that any apparatus to guide connector 46 for processor 40 into connector 44 for motherboard 20, falls within the scope of the invention. Similarly, various locking mechanisms can be used with the present  
20 invention and still fall within the scope of the invention. For example, the apparatus to prevent processor 40 from being removed could comprise a clear plate or lattice structure for covering slot 36 in case 30, and a lock assembly for locking the plate or lattice structure to case 30.

What is claimed is:

- 1 1. A mount to receive a cartridge processor having an  
2 edge connector, comprising:  
3 a motherboard connector to be mounted on a motherboard  
4 to receive said edge connector with said processor in an  
5 orientation parallel to said motherboard.
- 1 2. The mount of claim 1 further comprising:  
2 guides to restrain said processor from movement.
- 1 3. The mount of claim 2, wherein said guides comprise a  
2 pair of guide rails attached to said processor, and a pair  
3 of guide slots attached to said motherboard, with said  
4 guide slots configured to receive said guide rails.
- 1 4. The mount of claim 1, further comprising a case  
2 surrounding said motherboard, said case having a slot  
3 through which said connector can be inserted into said  
4 receiving slot without removing said case.
- 1 5. The mount of claim 3, wherein said case has an  
2 interior side and an exterior side, and wherein said  
3 processor is visible from said exterior side.
- 1 6. The mount of claim 1, wherein said processor includes  
2 a heat sink.

1 7. The mount of claim 1, wherein said motherboard  
2 includes a heat sink, further comprising a heat sink  
3 connector for connecting said heat sink to said processor.

1 8. The mount of claim 1, further comprising a lock for  
2 preventing unauthorized removal of said processor from said  
3 motherboard.

1 9. The mount of claim 8, wherein said lock comprises  
2 teeth locks.

1 10. The mount of claim 8, wherein said lock comprises a  
2 spring lock assembly.

1 11. The mount of claim 8, wherein said lock comprises a  
2 clear plate for covering said slot in said case, and a lock  
3 assembly for locking said plate to said case.

1 12. A mount for a processor, comprising:  
2 a motherboard;  
3 a receiving slot connected to said motherboard and  
4 configured to receive a connector for a processor;  
5 guides for guiding said connector into said receiving  
6 slot, wherein said processor is horizontal to said  
7 motherboard after said connector is inserted into said  
8 receiving slot.



1 13. The mount of claim 12, wherein said guides include a  
2 pair of guide slots connected to said motherboard, and a  
3 pair of guide rails connected to said processor, with said  
4 guide slots configured to receive said guide rails.

1 14. A mount for a processor, comprising:  
2 a motherboard;  
3 a single edge connector cartridge processor having a  
4 connector;  
5 a receiving slot connected to said motherboard and  
6 configured to receive said connector;  
7 guides for guiding said connector into said receiving  
8 slot, wherein said processor is horizontal to said  
9 motherboard after said connector is inserted into said  
10 receiving slot.

1 15. The mount of claim 13, wherein said guides comprise a  
2 pair of guide rails connected to said processor, and a pair  
3 of guide slots connected to said motherboard, with said  
4 guide slots configured to receive said guide rails.

1 16. A method of mounting a cartridge processor having an  
2 edge connector on a motherboard comprising:  
3 inserting the edge connector of said processor in a  
4 connector on said motherboard, said processor having an  
5 orientation parallel to said motherboard.

1 17. The method of claim 16 wherein in said inserting step,  
2 said processor is inserted in said motherboard in a first  
3 direction, the method further comprising:  
4       restraining said processor from movement in directions  
5 mutually perpendicular to said first direction.

1 18. A horizontal mount for a processor, comprising:  
2       guides to guide a connector into a receiving slot on a  
3 motherboard; and  
4       a single edge connector cartridge processor having a  
5 connector coupled to said motherboard in a direction  
6 parallel to said motherboard.

ABSTRACT OF DISCLOSURE:

1        One embodiment of the present invention comprises a  
2 mount to receive a cartridge processor having an edge  
3 connector. The mount includes a motherboard connector  
4 mounted on a motherboard oriented to receive the edge  
5 connector with the processor in an orientation parallel to  
6 the board. Further, the mount may include guides for  
7 restraining said processor from movement.

2025 RELEASE UNDER E.O. 14176

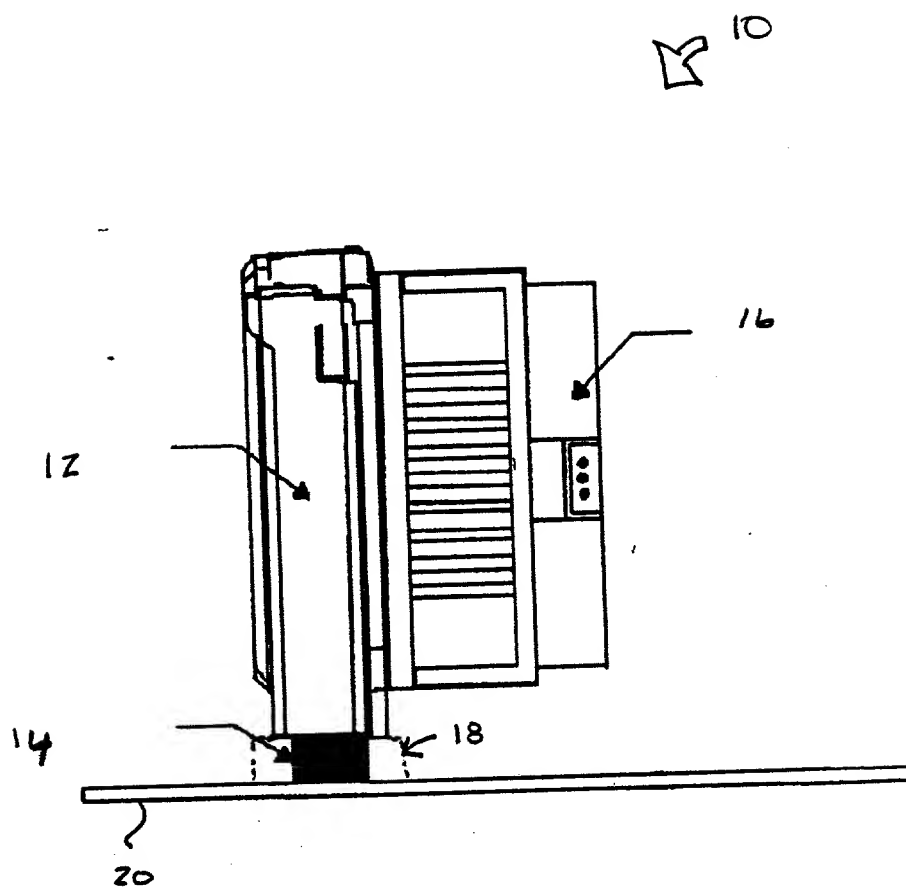


FIG. 1 (PRIOR ART)

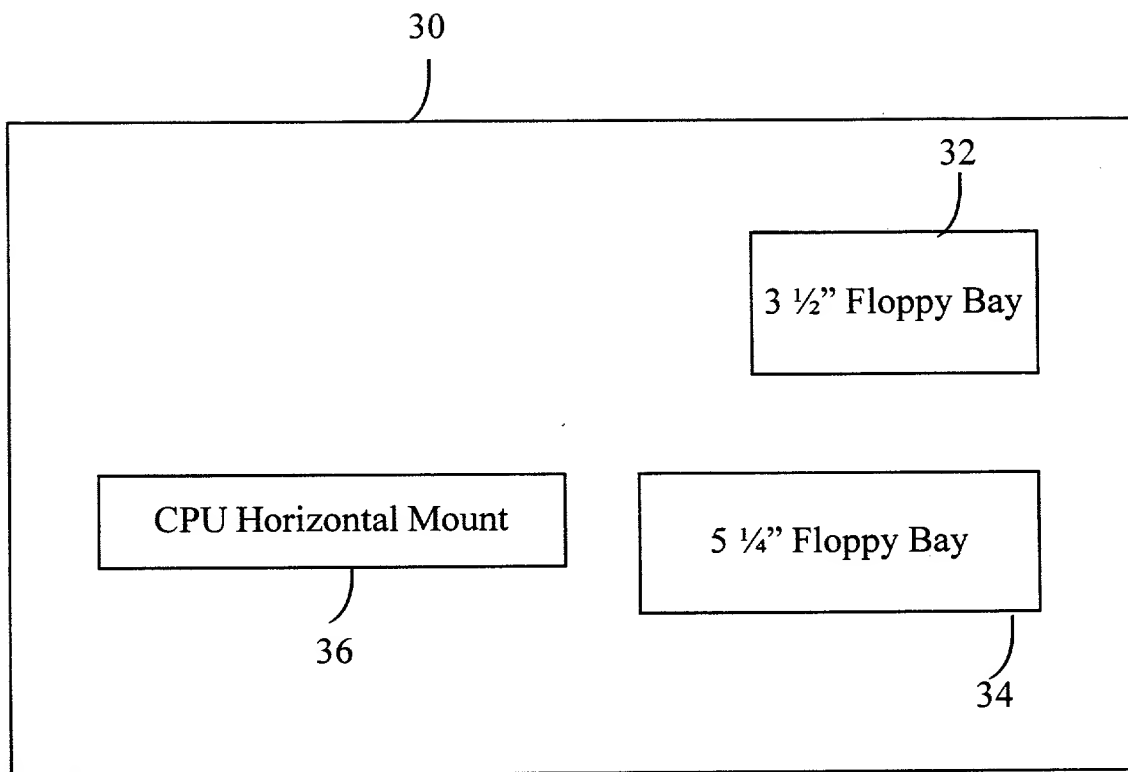


FIG. 2

The diagram illustrates a device 20, which appears to be a container or a frame assembly. The top view shows a rectangular frame 41 with a central opening 44. The frame is flanked by side rails 42 and 43. A component 36 is positioned below the frame. The side view shows the frame 40 with a top rail 45 and a bottom rail 46, with a central opening 48.

FIG. 3

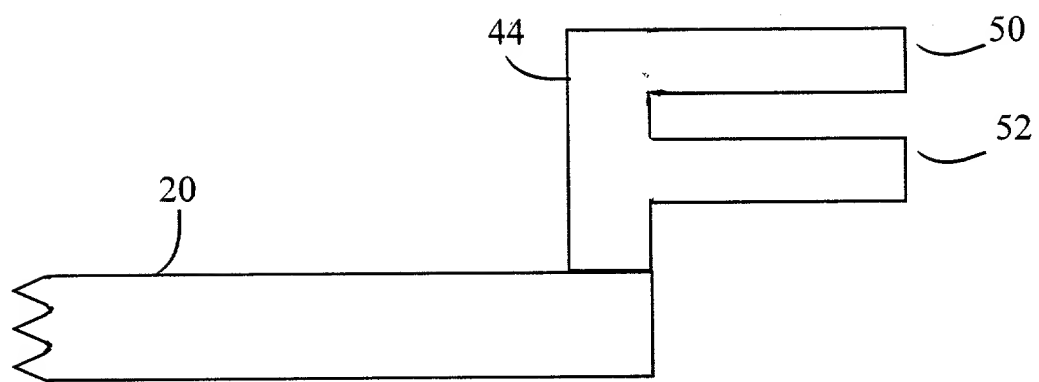


FIG. 4

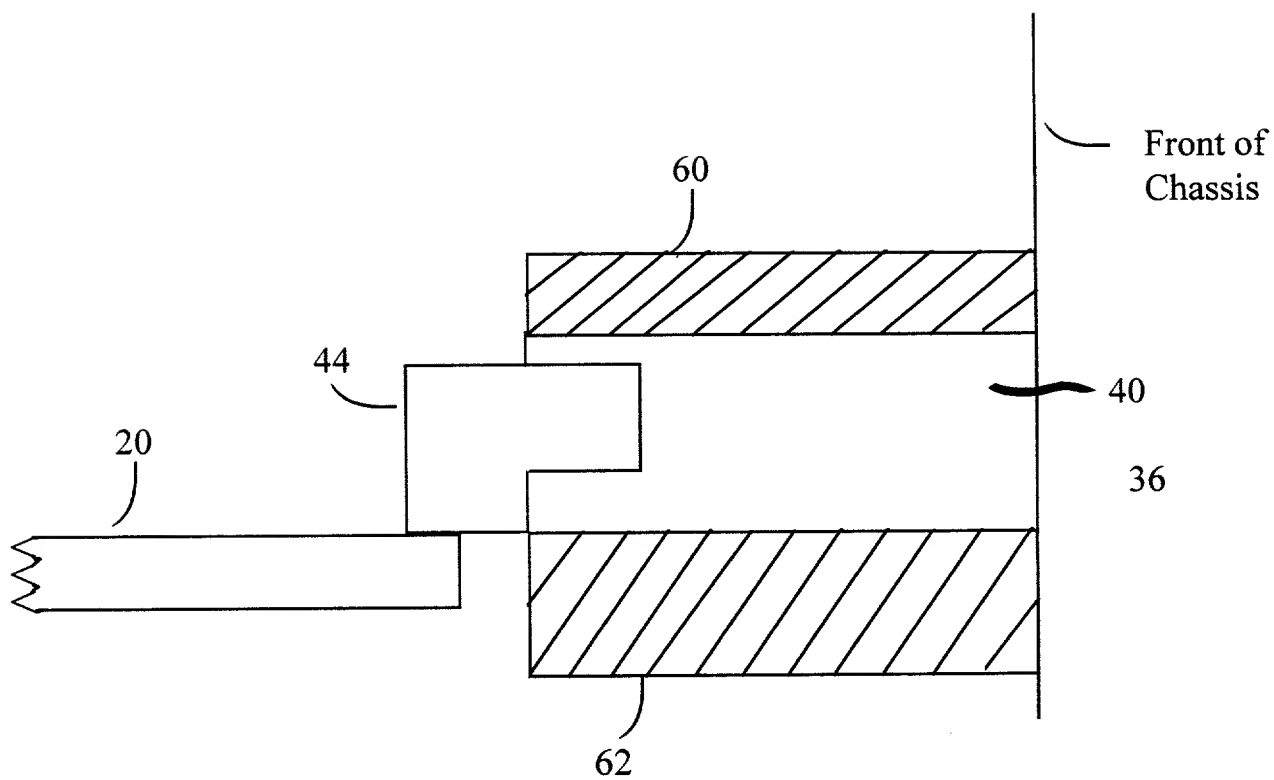


FIG. 5



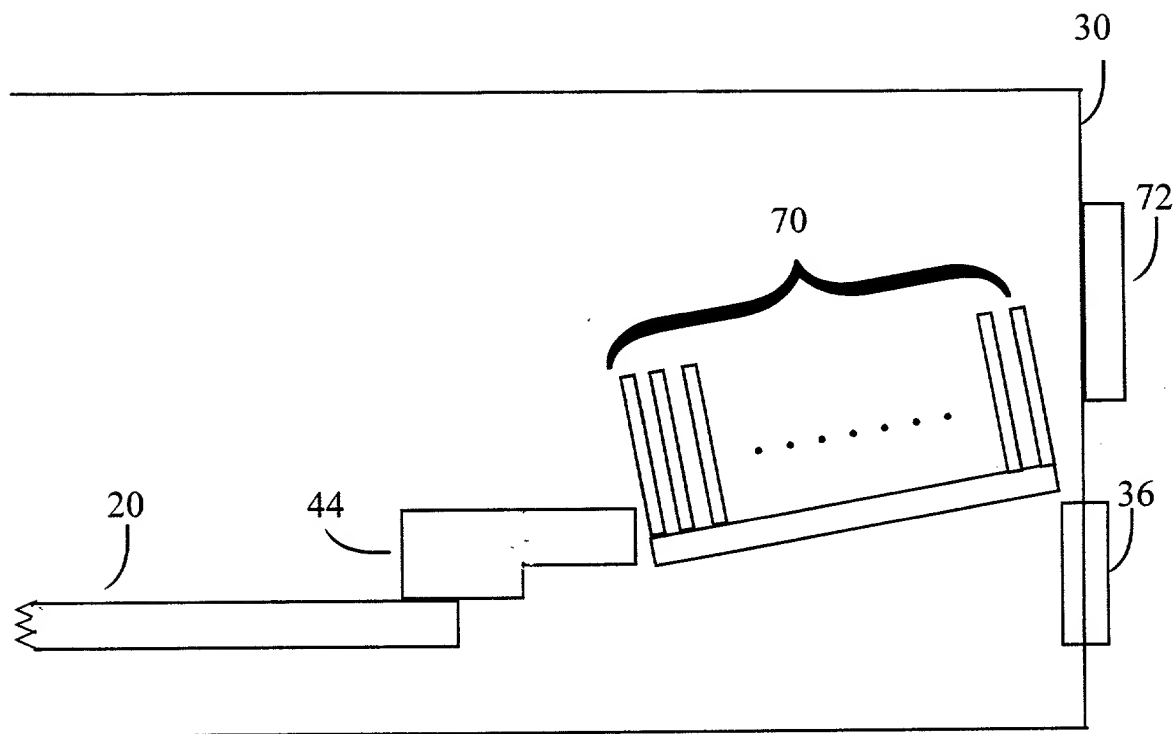


FIG. 6

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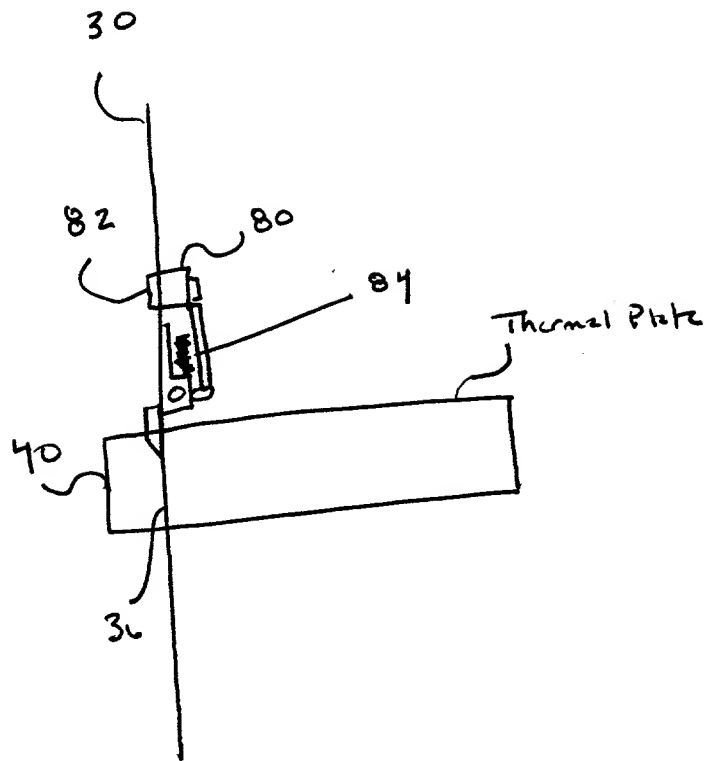


FIG. 7

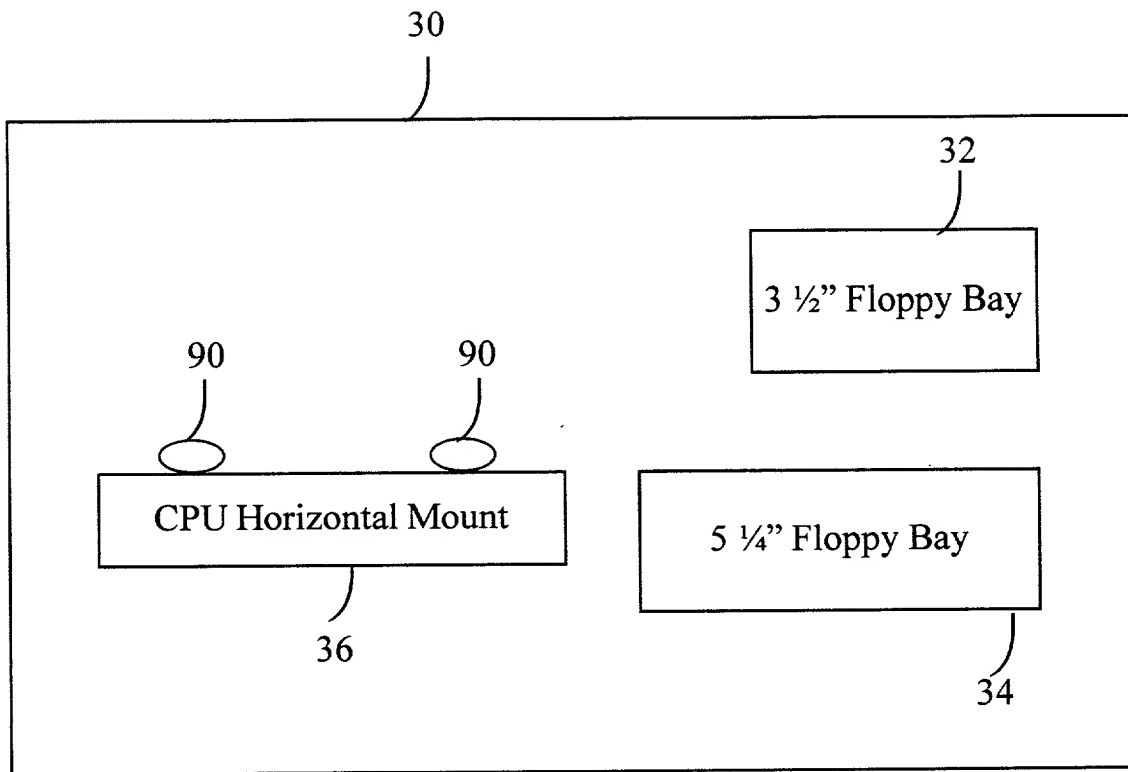


FIG. 8

ATTORNEY'S DOCKET NO. 2207/4641

**PATENT**

**DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION**

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name.

I believe I am the original, first, and sole inventor of the subject matter that is claimed and for which a patent is sought on the invention entitled **A HORIZONTAL MOUNT FOR A PROCESSOR**,

the specification of which

  X   is attached hereto.

\_\_\_\_\_ was filed on \_\_\_\_\_ as United States Application Number \_\_\_\_\_ or PCT International Application Number \_\_\_\_\_ and was amended on \_\_\_\_\_ (if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claim(s), as amended by any amendment referred to above. I do not know and do not believe that the claimed invention was ever known or used in the United States of America before my invention thereof, or patented or described in any printed publication in any country before my invention thereof or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, and that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns more than twelve months (for a utility patent application) or six months (for a design patent application) prior to this application.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a), a copy of which is attached.

**PRIOR FOREIGN APPLICATION(S)**

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d), of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

APPLICATION NUMBER	COUNTRY	FILING DATE (day, month, year)	PRIORITY CLAIMED Yes                      No

**PRIOR UNITED STATES APPLICATION(S)**

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application.

APPLICATION NUMBER	FILING DATE (day, month, year)	STATUS (i.e. Patented, Pending, Abandoned)


**POWER OF ATTORNEY:** I hereby appoint:

Paul H. Heller (Reg. No. 21,074); John C. Aftuniller (Reg. No. 25,951); Felix L. D'Arienzo, Jr. (Reg. No. 27,631); Shawn W. O'Dowd (Reg. No. 34,687) of KENYON & KENYON with offices located at 1025 Connecticut Ave., N.W., Washington, D.C. 20036, telephone (202) 429-1776, and James E. Jacobson, Jr. (Reg. No. 31,626); Thomas C. Reynolds (Reg. No. 32,488); Raymond J. Werner (Reg. No. 34,752); Richard C. Calderwood (Reg. No. 35,468); Joseph R. Bond (Reg. No. 36,458); Naomi Obinata (Reg. No. 39,320) of INTEL CORPORATION my attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith.

SEND CORRESPONDENCE, AND DIRECT TELEPHONE CALLS TO:

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I hereby declare that all statements made herein of my own knowledge are true and all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under § 1001 of Title 18 of the United States Code and that such willful statements may jeopardize the validity of the application or any patent issuing thereon.

FULL NAME OF INVENTOR	FAMILY NAME Farmer	FIRST GIVEN NAME Christopher	SECOND GIVEN NAME B.
RESIDENCE & CITIZENSHIP	CITY Tigard	STATE OR FOREIGN COUNTRY Oregon	COUNTRY OF CITIZENSHIP U.S.A.
POST OFFICE ADDRESS	POST OFFICE ADDRESS 15350 SW Firtree Drive	CITY Tigard	STATE & ZIP CODE/COUNTRY OR 97223/USA
Signature 		Date 19 28th NOV 28, 1997	

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**Title 37, Code of Federal Regulations, Section 1.56**  
**Duty to Disclose Information Material to Patentability**

(a) A patent by its very nature is affected with a public interest. The public interest is best served, and the most effective patent examination occurs when, at the time an application is being examined, the Office is aware of and evaluates the teachings of all information material to patentability. Each individual associated with the filing and prosecution of a patent application has a duty of candor and good faith in dealing with the Office, which includes a duty to disclose to the Office all information known to that individual to be material to patentability as defined in this section. The duty to disclose information exists with respect to each pending claim until the claim is cancelled or withdrawn from consideration, or the application becomes abandoned. Information material to the patentability of a claim that is cancelled or withdrawn from consideration need not be submitted if the information is not material to the patentability of any claim remaining under consideration in the application. There is no duty to submit information which is not material to the patentability is deemed to be satisfied if all information known to be material to patentability of any claim issued in a patent was cited by the Office or submitted to the Office in the manner prescribed by §§ 1.97(b)-(d) and 1.98. However, no patent will be granted on an application in connection with which fraud on the Office was practiced or attempted or the duty of disclosure was violated through bad faith or intentional misconduct. The Office encourages applicants to carefully examine:

(1) Prior art cited in search reports of a foreign patent office in a counterpart application, and

(2) The closest information over which individuals associated with the filing or prosecution of a patent application believe any pending claim patentably defines, to make sure that any material information contained therein disclosed to the Office.

(b) Under this section, information is material to patentability when it is not cumulative to information already of record or being made of record in the application, and:

(1) It establishes, by itself or in combination with other information, a prima facie case of unpatentability of a claim; or

(2) It refutes, or is inconsistent with, a position the applicant takes in:

(i) Opposing an argument of unpatentability relied on by the Office, or

(ii) Asserting an argument of patentability.

A prima facie case of unpatentability is established when the information compels a conclusion that a claim is unpatentable under the preponderance of evidence, burden-of-proof standard, giving each term in the claim its broadest reasonable construction consistent with the specification, and before any consideration is given to evidence which may be submitted in an attempt to establish a contrary conclusion of patentability.

(c) Individuals associated with the filing or prosecution of a patent application within the meaning of this section are:

(1) Each inventor named in the application;

(2) Each attorney or agent who prepares or prosecutes the application; and

(3) Every other person who is substantively involved in the preparation or prosecution of the application and who is associated with the inventor, with the assignee or with anyone to whom there is an obligation to assign the application.

(d) Individuals other than the attorney, agent or inventor may comply with this section by disclosing information to the attorney, agent, or inventor.

2025-11-19 15:34 FAX 503 681 8711